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<b>RESCISSON OF PREVIOUS NONPUBLICATION REQUEST</b> (35 U.S.C. 122(b)(2)(B)(ii)) <b>AND, IF APPLICABLE,</b> <b>NOTICE OF FOREIGN FILING</b> (35 U.S.C. 122(b)(2)(B)(iii))		Application Number	10/679,023
		Filing Date	October 3, 2003
		First Named Inventor	Edmund J. Balboni
		Title	Phase-locked Loop Bandwidth Calibration Circuit and Method Thereof
		Atty Docket Number	Analog.7150
		Art Unit	2858
		Examiner	Unknown

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A request that the above-identified application not be published under 35 U.S.C. 122(b) (nonpublication request) was included with the above-identified application on filing pursuant to 35 U.S.C. 122(b)(2)(B)(i).  
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Signature

Date

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Matthew E. Connors

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33,298

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Typed or printed name

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This collection of information is required by 37 CFR 1.213(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 6 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop PG Pub, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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